

Special Issue on

High Frequency Modulator and Demodulator Analysis and Design



Call For Papers

A new Special Issue *High Frequency Modulator and Demodulator Analysis and Design* (<http://www.eeejournal.org/sinfo/239019>) has been launched in *Journal of Electrical and Electronic Engineering* (<http://www.eeejournal.org>). This Special Issue intends to collect research achievements concerning High Frequency Modulator and Demodulator. Novel insights as well as fundamental research on the topics are warmly welcomed. Your submissions along with your ingenious works are expected.

Lead Guest Editor

Lead Guest Editor: Kittipong Tripetch

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Paper Submission

Potential authors are humbly requested to submit an electronic copy of their complete manuscript via <http://www.eeejournal.org/submission>

Topics of Interest Include (but not limited to):

- ◆ Show the simulation results
- ◆ Request how to analyze the circuit diagram of the modulator and demodulator
- ◆ Review and compare, comments the modulator and demodulators from IEEE Journal of solid state circuit and circuit and system
- ◆ Show the experimental results
- ◆ Request novel circuit diagram and block diagram of modulator and demodulator (PLL or Costas Loops)
- ◆ Compute the specifications of the modulator and demodulator by a figure of merit of the modulator

Important Dates

Submission Deadline: **Feb. 20, 2020**

Publication Deadline: **Apr. 20, 2020**



Join as Guest Editor

For scholars who have intention to join the special issue as guest editor, please check out the link below:

<http://www.eeejournal.org/jsjt/239019>